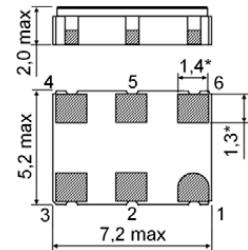
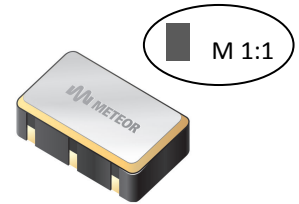


**Electrical Performance**

Parameter	Units	Value
Frequency range: - LVCMOS (CM) - LVPECL (PE) & LVDS (DS)	MHz MHz	0.75 to 300 0.75 to 800
Frequency tolerance,(code)	ppm	±10.0(5); ±15.0(6); ±20.0(7)
Temperature range	°C (code)	-10 ... 60 (A) -40 ... 85 (C) -60 ... 85 (D)
Frequency stability: A C D	ppm (code)	±20(P);±30(C);±40(T);±50(U);±100(H) ±30(C);±40(T);±50(U);±100(H) ±40(T);±50(U);±100(H)
Output Level: - Duty Cycle	%	40 ... 60
Output voltage (for LVDS)	mV	250
Stability: - Power Supply, ±10% change - Load :	ppm	±2.0 max
- 15 at 30 pF	ppm	±2.0 max
- 10 at 15 pF	ppm	±2.0 max
Supply voltage (Up)		3.3±10%
Supply Current: - forLVCMOS 0.75 to 24 MHz 24 - 96 MHz ov. 96 MHz - for LVPECL 0.75 to 24 MHz 24 - 96 MHz ov. 96 MHz - for LVDS 0.75 to 24 MHz 24 - 96 MHz ov. 96 MHz	mA mA mA mA mA mA mA mA mA mA mA	15 max 30 max 65 max 60 max 65 max 100 max 28 max 45 max 80 max
Period Jitter		5.0
Rise and fall times: - for LVPECL - for LVDS	nc nc	1.5 1.0



Ceramic package with metal lid  
Plating: Ni+Au(0,3...1 μm)

**Pinout**

1	Control Voltage
2	Ground
3	Output
4	Supply Voltage
5, 6, 7, 8	Make No Connection

**Environmental**

Shock:  
test Ea. 1500 gn acceleration for 0.1-0.2 ms duration, half sine pulse, 2 shocks in each direction along three mutually perpendicular axes at octave per minute

Vibration:  
test Fc. 50Hz 2.0 mm displacement, 1-500 Hz at 10 gn, 8 hours in each of three mutually perpendicular axes at 1 octave per minute

Storage temperature: -60°C to 85°C

**Long Term Frequency Stability**

- ±25 ppm max for 25 years  
- ±20 ppm max in 1st year

**Ordering Information**
**PCXO GK326-1-S-CP-40M-3.3PE**
